

### POWER FIELD EFFECT TRANSISTOR

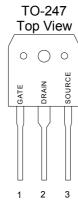
#### **GENERAL DESCRIPTION**

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

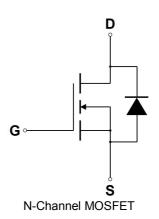
# FEATURES Robust High

- Robust High Voltage Termination
- Avalanche Energy Specified
- ◆ Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- ◆ Diode is Characterized for Use in Bridge Circuits
- ◆ I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature
- Isolated Mounting Hole Reduces Mounting Hardware

#### PIN CONFIGURATION



### **SYMBOL**



### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Prain to Current — Continuous		75.3	_
- Pulsed	$I_{DM}$	225.9 A	
Gate-to-Source Voltage — Continue	$V_{GS}$	±20	V
Total Power Dissipation –TO-247	$P_D$	595	W
Derate above 25℃ -TO-247		4.76	W/°C
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$ C ( $V_{DD} = 100V$ , $V_{GS} = 10V$ , $I_L = 17A$ , $L = 20mH$ , $R_G = 25\Omega$ )	E <sub>AS</sub>	2890	mJ
Thermal Resistance — Junction to Case -TO-247	θ <sub>JC</sub>	0.21	
			°C/W
<ul> <li>Junction to Ambient -TO-247</li> </ul>	$\theta_{JA}$	40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

<sup>(1)</sup> Drain current limited by maximum junction temperature



### Power Field Effect Transistor

### **ORDERING INFORMATION**

Part Number	TOP MARK	Part Number	Packing Mthod	Note
GWM76S60XN247 (Notte1)	GWM76S60X	TO-247	Tube	
GWM76S60GN247 (Notte2)	GWM76S60G	TO-247	Tube	

Note1: X : Suffix for Halogen Free Product, Note2: G : Suffix for PB Free Product,

### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

			GWM76S60			
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	600			V
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$		V (BR)DSS	000			٧
Drain-Source Leakage Current (V <sub>DS</sub> =600 V, V <sub>GS</sub> = 0 V)		I <sub>DSS</sub>			1	uA
					!	uA
Gate-Source Leakage Current-Forward					100	24
$(V_{gsf} = 20 \text{ V}, V_{DS} = 0 \text{ V})$		I <sub>GSSF</sub>			100	nA
Gate-Source Leakage Current-Ro	Leakage Current-Reverse				100	nA
$(V_{gsr} = -20 \text{ V}, V_{DS} = 0 \text{ V})$		I <sub>GSSR</sub>			100	IIA
Gate Threshold Voltage		V <sub>GS(th)</sub>	2	3	4	V
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$						
Static Drain-Source On-Resistance (V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25.4A) *		R <sub>DS(on)</sub>		36	42	mΩ
Input Capacitance	$(V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}.$	C <sub>iss</sub>		6078		pF
Output Capacitance	f = 1.0 MHz)	Coss		247		pF
Reverse Transfer Capacitance	1 = 1.0 MH2)	C <sub>rss</sub>		25		pF
Turn-On Delay Time	$(V_{DD} = 300 \text{ V}, I_D = 76 \text{ A},$ $R_G = 25\Omega)$ *	t <sub>d(on)</sub>		48.9		ns
Rise Time		t <sub>r</sub>		115.2		ns
Turn-Off Delay Time		t <sub>d(off)</sub>		179.9		ns
Fall Time		t <sub>f</sub>		113.2		ns
Total Gate Charge	$(V_{DS} = 480 \text{ V}, I_{D} = 76 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	Qg		143.9		nC
Gate-Source Charge		Q <sub>gs</sub>		39.5		nC
Gate-Drain Charge		$Q_{gd}$		60.0		nC
SOURCE-DRAIN DIODE CHARA	ACTERISTICS					
Forward On-Voltage(1)	$(I_S = 76 \text{ A}, d_{IS}/d_t = 100\text{A/}\mu\text{s})$	$V_{SD}$			1.5	V
Forward Turn-On Time		ton		**		ns
Reverse Recovery Time		t <sub>rr</sub>		627		ns

<sup>\*</sup> Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%

<sup>\*\*</sup> Negligible, Dominated by circuit inductance



### TYPICAL ELECTRICAL CHARACTERISTICS

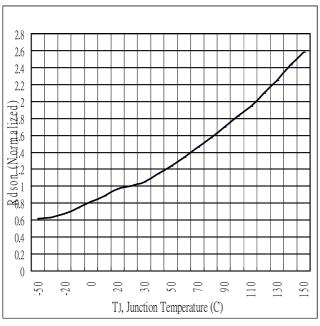


Fig 1. On-Resistance Variation with vs. Temperature

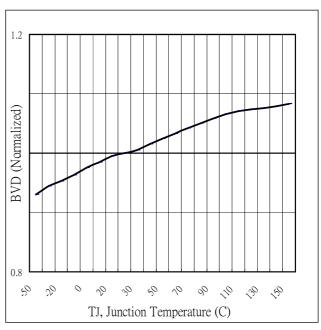


Fig.2 Breakdown Voltage Variation vs. Temperature

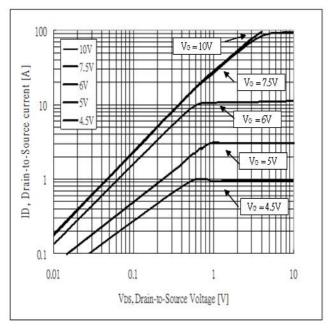


Fig 3. Typical Output Characteristics

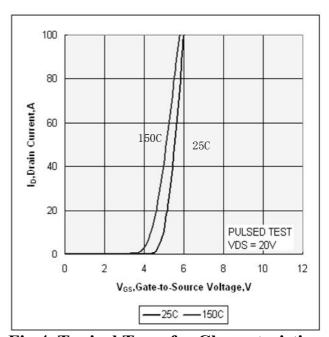


Fig 4. Typical Transfer Characteristics



## Power Field Effect Transistor

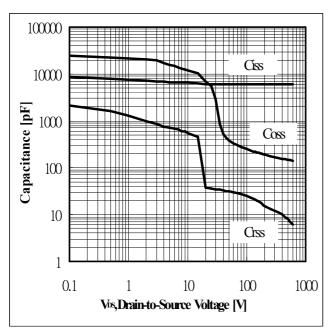


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

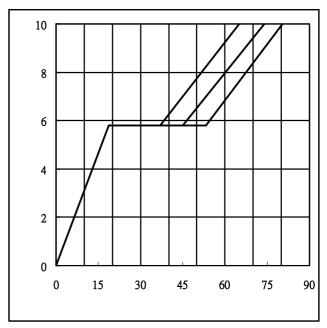


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage