



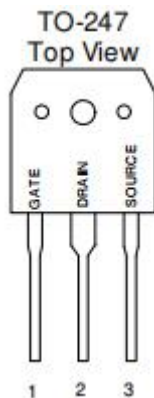
GENERAL DESCRIPTION

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

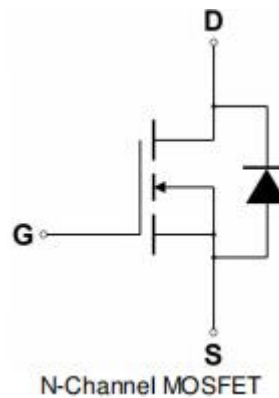
FEATURES

- ◆ Robust High Voltage Termination
- ◆ Avalanche Energy Specified
- ◆ Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- ◆ Diode is Characterized for Use in Bridge Circuits
- ◆ I_{DSS} and $V_{DS(on)}$ Specified at Elevated Temperature
- ◆ Isolated Mounting Hole Reduces Mounting Hardware

PIN CONFIGURATION



SYMBOL



ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current – Continuous	$I_{D 25C(1)}$	71.9	A
– Pulsed	$I_{D 100C(1)}$	45.5	
	I_{DM}	215.8	
Gate-to-Source Voltage – Continue	V_{GS}	±30	V
Total Power Dissipation–TO-247	P_D	581	W
TO-3P		595	
Derate above 25°C –TO-247		4.65	W/°C
TO-3P		4.76	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy – $T_J = 25^\circ\text{C}$ ($V_{DD} = 100\text{V}, V_{GS} = 10\text{V}, I_L = 17\text{A}, L = 20\text{mH}, R_G = 25\Omega$)	E_{AS}	2725	mJ
Thermal Resistance – Junction to Case –TO-247	θ_{JC}	0.21	°C/W
– Junction to Case TO-3P			
– Junction to Ambient TO-3P ,TO-247	θ_{JA}	40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T_L	260	°C

(1) Drain current limited by maximum junction temperature, TO-3P Package.



GWM72S60

POWER FIELD EFFECT TRANSISTOR

ORDERING INFORMATION

Part Number	TOP MARK	Part Number	Packing Method	Note
GWM72S60XN247(Note1)	GWM72S60	TO-247	Tube	
GWM72S60XN3P(Note1)	GWM72S60	TO-3P	Tube	

Note1: X : Suffix for Halogen Free Product,

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J = 25^\circ\text{C}$.

Characteristic	Symbol	GWM72S60			Units
		Min	Typ	Max	
Drain-Source Breakdown Voltage ($V_{GS} = 0\text{ V}$, $I_D = 250\ \mu\text{A}$)	$V_{(BR)DSS}$	600			V
Drain-Source Leakage Current ($V_{DS} = 600\text{ V}$, $V_{GS} = 0\text{ V}$)	I_{DSS}			10	μA
Gate-Source Leakage Current-Forward ($V_{gsf} = 30\text{ V}$, $V_{DS} = 0\text{ V}$)	I_{GSSF}			100	nA
Gate-Source Leakage Current-Reverse ($V_{gsr} = -30\text{ V}$, $V_{DS} = 0\text{ V}$)	I_{GSSR}			100	nA
Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$)	$V_{GS(th)}$	2		4	V
Static Drain-Source On-Resistance ($V_{GS} = 10\text{ V}$, $I_D = 25.4\text{A}$) *	$R_{DS(on)}$			46	$\text{m}\Omega$
Input Capacitance	$(V_{DS} = 100\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1.0\text{ MHz}$)	C_{iss}	5447		pF
Output Capacitance		C_{oss}	233		pF
Reverse Transfer Capacitance		C_{rss}	23		pF
Turn-On Delay Time	$(V_{DD} = 300\text{ V}$, $I_D = 72\text{ A}$, $R_G = 25\Omega$) *	$t_{d(on)}$	45.4		ns
Rise Time		t_r	111.1		ns
Turn-Off Delay Time		$t_{d(off)}$	155.7		ns
Fall Time		t_f	110		ns
Total Gate Charge	$(V_{DS} = 480\text{ V}$, $I_D = 72\text{ A}$, $V_{GS} = 10\text{ V}$)*	Q_g	127.6		nC
Gate-Source Charge		Q_{gs}	33.9		nC
Gate-Drain Charge		Q_{gd}	51.2		nC
SOURCE-DRAIN DIODE CHARACTERISTICS					
Forward On-Voltage(1)	$(I_S = 72\text{ A}$, $dI_S/dt = 100\text{A}/\mu\text{s}$)	V_{SD}		1.5	V
Forward Turn-On Time		t_{on}	**		ns
Reverse Recovery Time		t_{rr}	210		ns

* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

** Negligible, Dominated by circuit inductance



TYPICAL ELECTRICAL CHARACTERISTICS

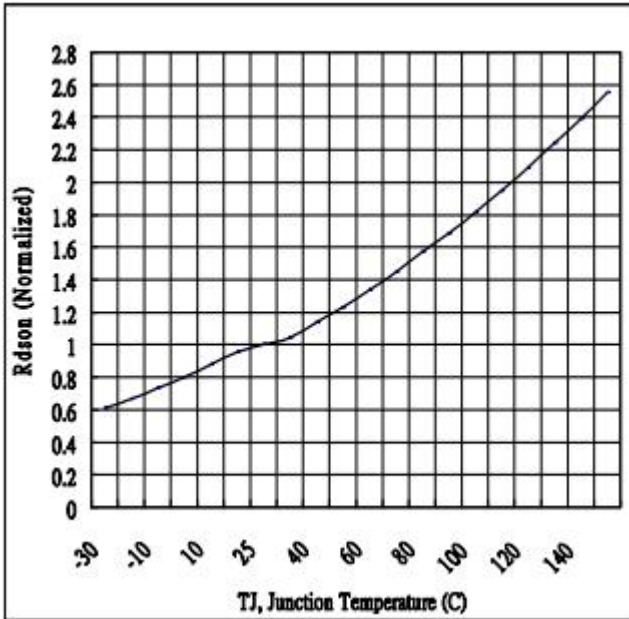


Fig 1. On-Resistance Variation with vs. Temperature

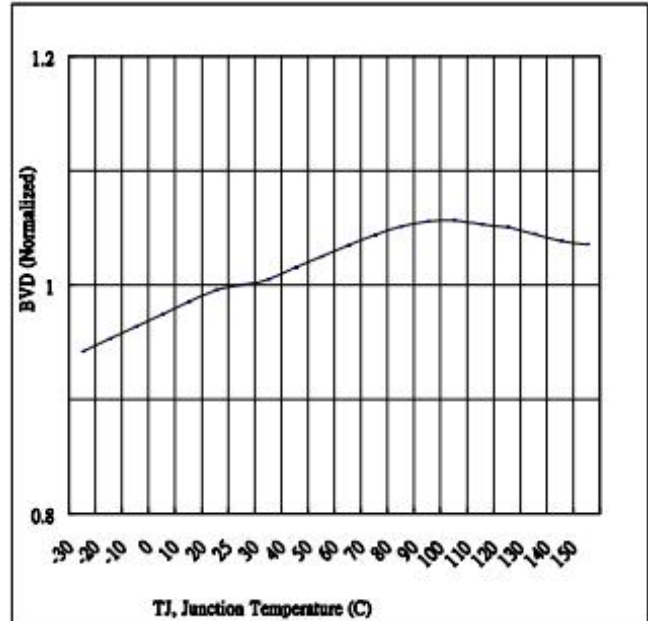


Fig.2 Breakdown Voltage Variation vs. Temperature

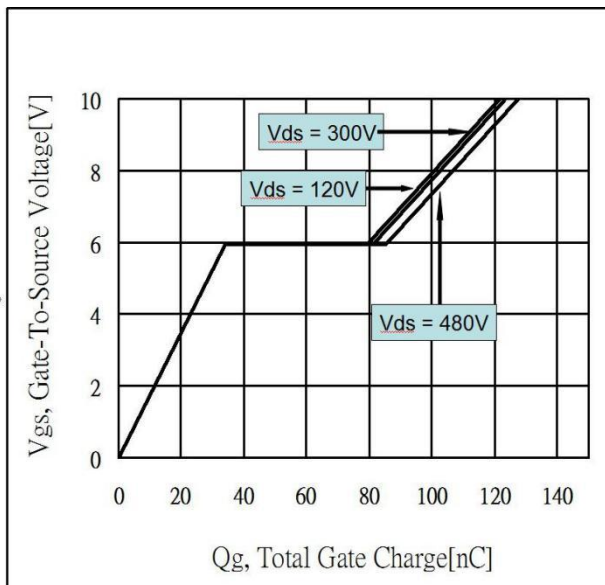


Fig 3. Typical Gate Charge vs. Gate-to-Source Voltage

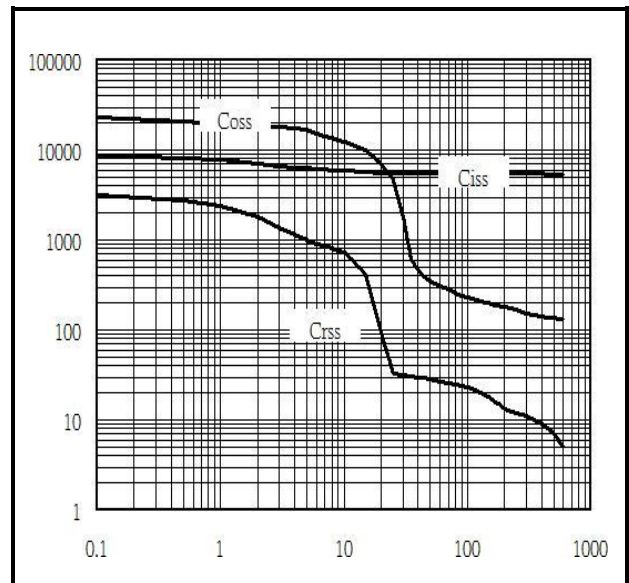


Fig 4. Typical Capacitance Vs. Drain-to-Source Voltage



GWM72S60

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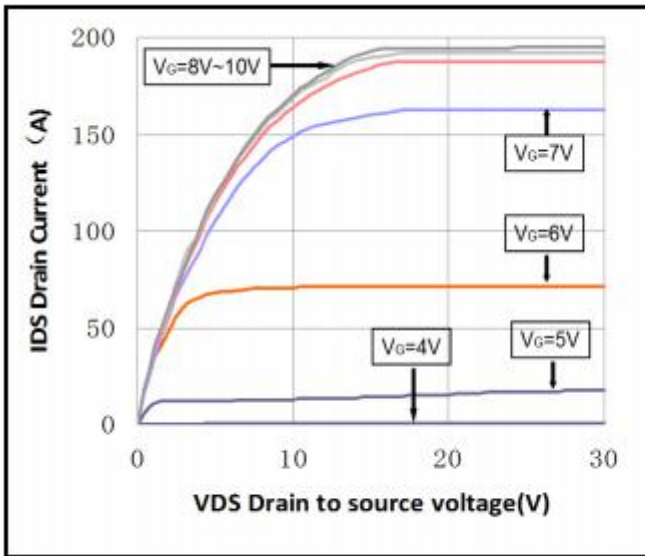


Fig 5. Typical Output Characteristics

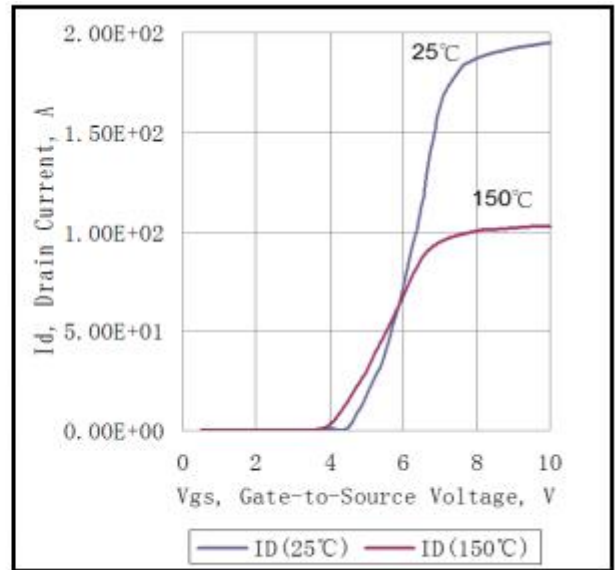


Fig 6. Typical Transfer Characteristics