GWM19S65

POWER FIELD EFFECT TRANSISTOR

U03GENERAL DESCRIPTION

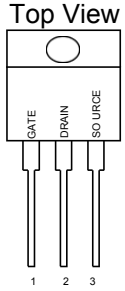
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

FEATURES

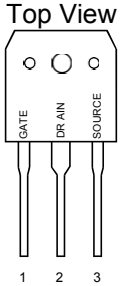
- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- I_{DSS} and V_{DS}(on) Specified at Elevated Temperature
- Isolated Mounting Hole Reduces Mounting Hardware

PIN CONFIGURATION

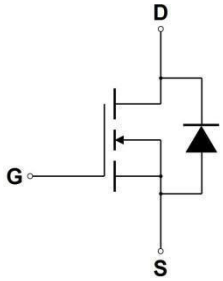
TO-220/TO-220F
Top View



TO-3P/T-O247
Top View



SYMBOL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current — Continuous	$I_{D\,(1)}$	18.6	A
— Pulsed	I_{DM}	55.8	
Gate-to-Source Voltage — Continue	V_{GS}	±20	V
Total Power Dissipation –TO-220	P_D	184	W
–TO-220FP		36	
–TO-3P		198	
–TO2-47		184	
Derate above 25℃ –TO-220		1.47	W/℃
–TO-220FP		0.29	
–TO-3P		1.59	
–TO-24		1.47	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	℃
Single Pulse Drain-to-Source Avalanche Energy — T_J = 25℃ (V_{DD} = 100V, V_{GS} = 10V, I_L = 6.8A, L = 10mH, R_G = 25Ω)	E_{AS}	271.2	mJ
Thermal Resistance — Junction to Case -TO-220	θ_{JC}	0.68	℃/W
— Junction to Case -TO-220FP		3.5	
— Junction to Case -TO-3P		0.63	
— Junction to Case -TO-247		0.68	
— Junction to Ambient -TO-220 , TO-220FP	θ_{JA}	62.5	
— Junction to Ambient -TO-3P , TO-247		40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T_L	260	℃

(1) Drain current limited by maximum junction temperature (TO3P)

ORDERING INFORMATION

Part Number	TOP MARK	Part Number	Packing Mthod	Note
GWM19S65N220	GWM19S65	TO-220	Tube	
GWM19S65N220FP	GWM19S65	TO-220FP	Tube	
GWM19S65N3P	GWM19S65	TO-3P	Tube	
GWM19S65N247	GWM19S65	TO-247	Tube	

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, T_J = 25℃.

			GWM19S65			
Characteristic		**Symbol**	**Min**	**Typ**	**Max**	**Units**
Drain-Source Breakdown Voltage (V_{GS} = 0 V, I_D = 250 μA)		$V_{(BR)DSS}$	650			V
Drain-Source Leakage Current (V_{DS} =650 V, V_{GS} = 0 V)		I_{DSS}			1	uA
Gate-Source Leakage Current-Forward (V_{gsf} = 20 V, V_{DS} = 0 V)		I_{GSSF}			100	nA
Gate-Source Leakage Current-Reverse (V_{gsr} = - 20 V, V_{DS} = 0 V)		I_{GSSR}			100	nA
Gate Threshold Voltage (V_{DS} = V_{GS}, I_D = 250 μA)		$V_{GS(th)}$	2	3.5	4	V
Static Drain-Source On-Resistance (V_{GS} = 10 V, I_D = 6.2A) *		$R_{DS(on)}$			230	mΩ
Gate resistance (f=1MHz, open drain)		R_G		2.7		Ω
Input Capacitance	(V_{DS} = 100 V, V_{GS} = 0 V, f = 1.0 MHz)	C_{iss}		1500		pF
Output Capacitance		C_{oss}		59		pF
Reverse Transfer Capacitance		C_{rss}		32		pF
Turn-On Delay Time	(V_{DD} = 325 V, I_D = 19 A, R_G = 25Ω) *	$t_{d(on)}$		18.8		ns
Rise Time		t_r		45.6		ns
Turn-Off Delay Time		$t_{d(off)}$		37.4		ns
Fall Time		t_f		58.6		ns
Total Gate Charge	(V_{DS} = 520 V, I_D = 19 A, V_{GS} = 10 V)*	Q_g		41.7		nC
Gate-Source Charge		Q_{gs}		9.3		nC
Gate-Drain Charge		Q_{gd}		17.9		nC
SOURCE-DRAIN DIODE CHARACTERISTICS						
Forward On-Voltage(1)	(I_S =19 A, d_{IS}/d_t = 100A/μs)	V_{SD}			1.5	V
Forward Turn-On Time		t_{on}		**		ns
Reverse Recovery Time		t_{rr}		388.93		ns

* Pulse Test: Pulse Width ≦300μs, Duty Cycle ≦2%

** Negligible, Dominated by circuit inductance

TYPICAL ELECTRICAL CHARACTERISTICS

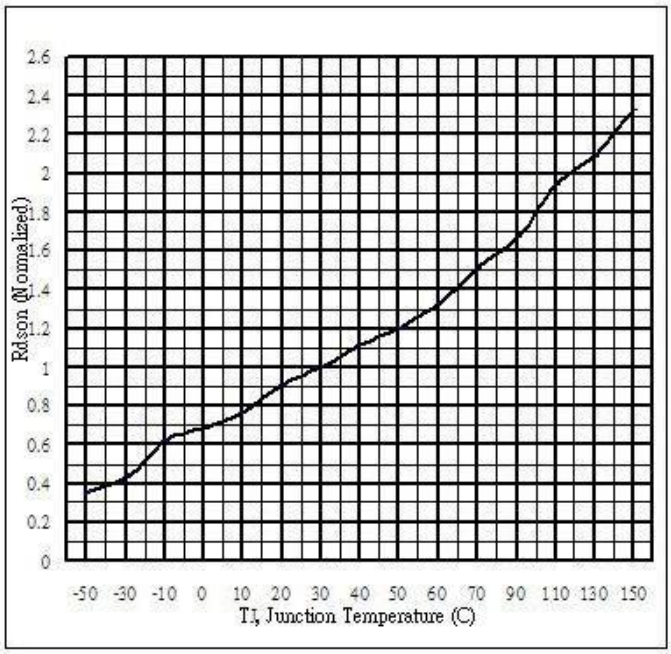


Fig 1. On-Resistance Variation with vs. Temperature

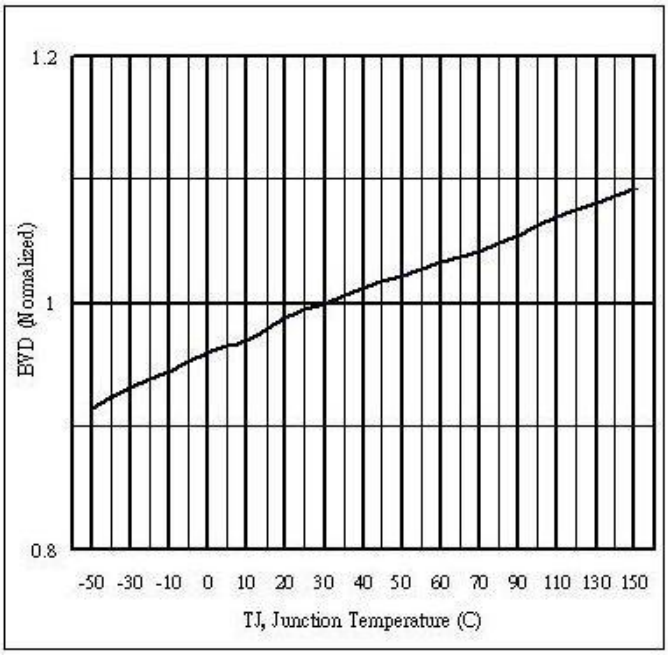


Fig.2 Breakdown Voltage Variation vs. Temperature

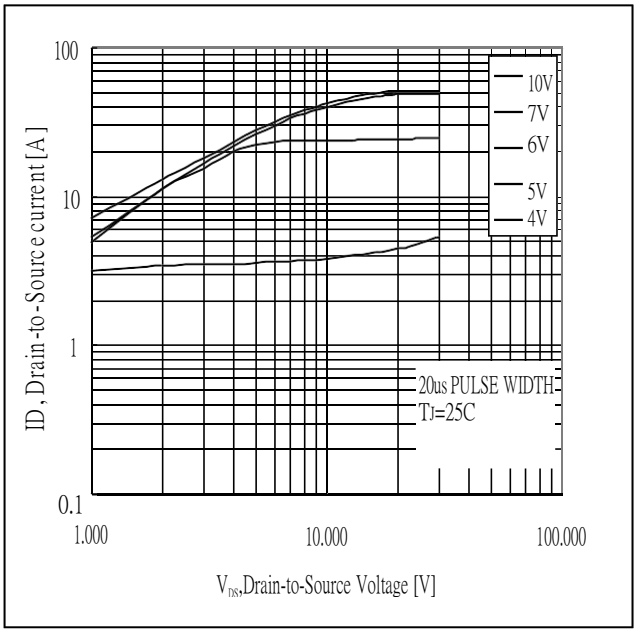


Fig 3. Typical Output Characteristics

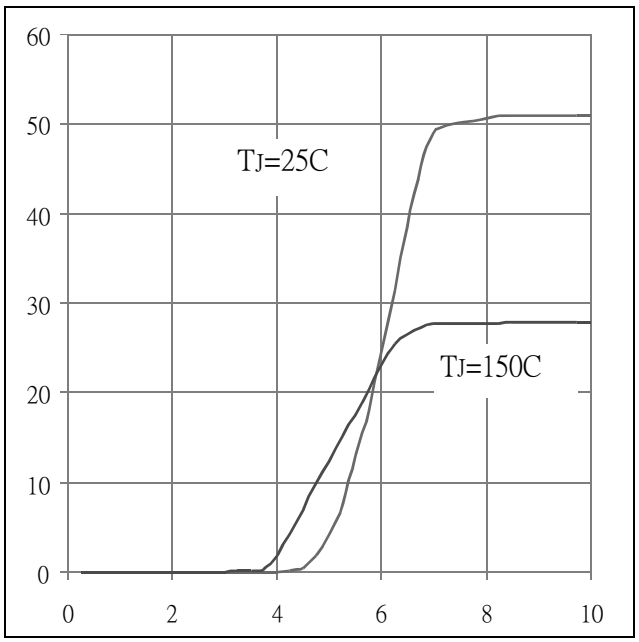


Fig 4. Typical Transfer Characteristics

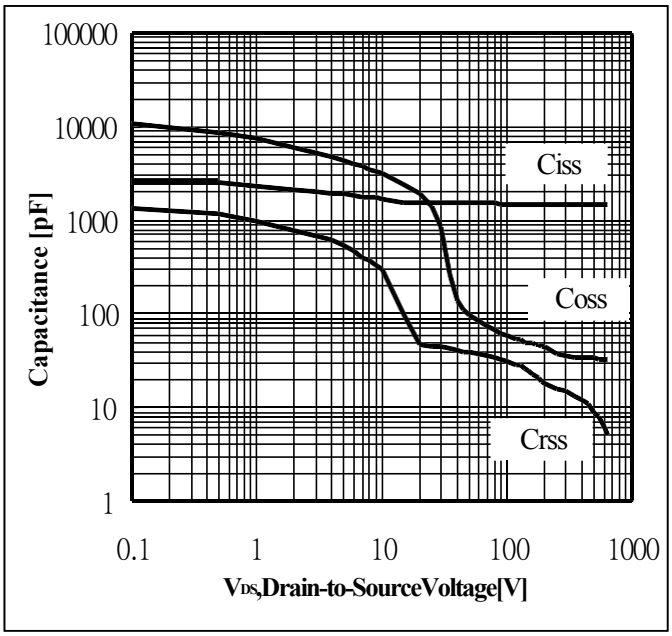


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

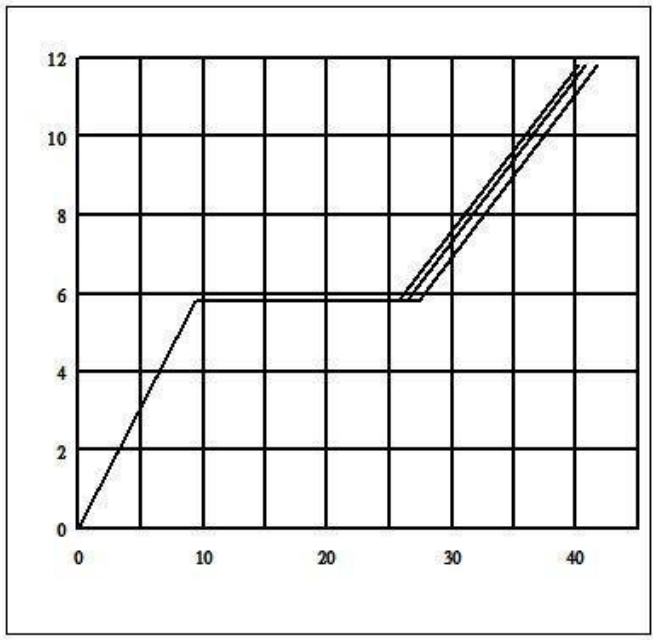


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage