



# GWM18S50

## POWER FIELD EFFECT TRANSISTOR

### GENERAL DESCRIPTION

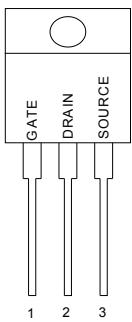
This advanced high voltage MOSFET is designed to withstand high energy in the avalanche mode and switch efficiently. This new high energy device also offers a drain-to-source diode with fast recovery time. Designed for high voltage, high speed switching applications such as power supplies, converters, power motor controls and bridge circuits.

### FEATURES

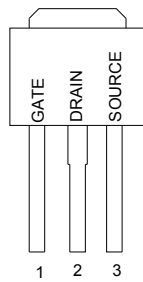
- ◆ SJ MOS
- ◆ Higher Current Rating
- ◆ Lower  $R_{ds(on)}$
- ◆ Lower Capacitances
- ◆ Lower Total Gate Charge

### PIN CONFIGURATION

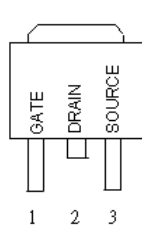
TO-220/TO-220FP  
Top View



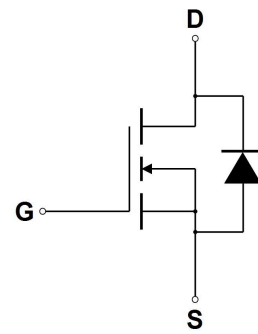
TO-251  
Front View



TO-252  
Front View



### SYMBOL



N-Channel MOSFET

### ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Drain to Current — Continuous	$I_D$	18	A	
— Pulsed	$I_{DM}$	54		
Gate-to-Source Voltage — Continue	$V_{GS}$	$\pm 20$	V	
Total Power Dissipation TO-251/TO-252	$P_D$	27	W	
TO-220		125		
TO-220FP		31		
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^{\circ}C$	
Single Pulse Drain-to-Source Avalanche Energy — $T_J = 25^{\circ}C$ ( $V_{DD} = 100V, V_{GS} = 10V, I_L = 5A, L = 10mH, R_G = 25\Omega$ )	$E_{AS}$	125	mJ	
Thermal Resistance — Junction to Case	$\theta_{JC}$	TO-251/TO-252	$^{\circ}C/W$	
		TO-220		4.6
		TO-220FP		1
— Junction to Ambient	$\theta_{JA}$	TO-251/TO-252	$^{\circ}C/W$	
		TO-220/ TO-220FP		4
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	$T_L$	260	$^{\circ}C$	



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### ORDERING INFORMATION

Part Number	TOP MARK	Part Number	Packing Method	Note
GWM18S50XN251 (Note1)	GWM18S50X	TO-251	Tube	
GWM18S50XN252 (Note1)	GWM18S50X	TO-252	Tube	
GWM18S50XN252TR (Note1)	GWM18S50X	TO-252	Tape and Reel	
GWM18S50XN220 (Note1)	GWM18S50X	TO-220	Tube	
GWM18S50XN220FP (Note1)	GWM18S50X	TO-220FP	Tube	
GWM18S50GN251 (Note2)	GWM18S50G	TO-251	Tube	
GWM18S50GN252 (Note2)	GWM18S50G	TO-252	Tube	
GWM18S50GN252TR (Note2)	GWM18S50G	TO-252	Tape and Reel	
GWM18S50GN220 (Note2)	GWM18S50G	TO-220	Tube	
GWM18S50GN220FP (Note2)	GWM18S50G	TO-220FP	Tube	

**Note1:** X : Suffix for Halogen Free and PB Free Product

**Note2:** G : Suffix for PB Free Product

### ELECTRICAL CHARACTERISTICS

Unless otherwise specified,  $T_J = 25^\circ\text{C}$ .

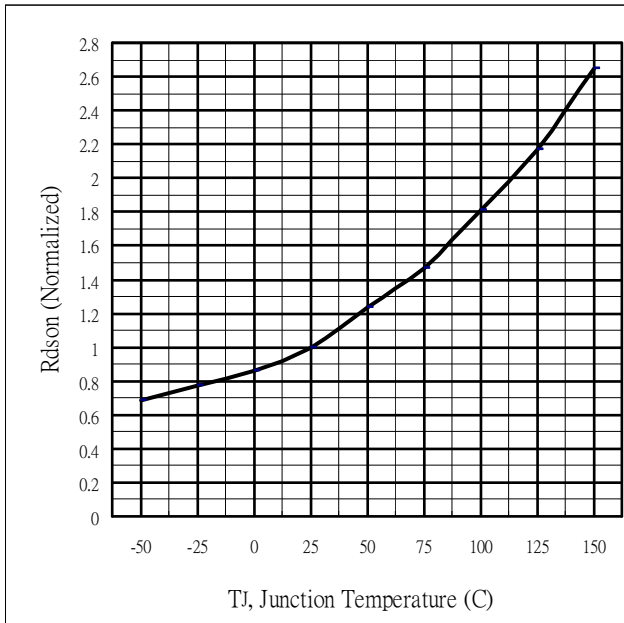
Characteristic		Symbol	GWM18S50			Units
			Min	Typ	Max	
Drain-Source Breakdown Voltage ( $V_{GS} = 0V, I_D = 250 \mu A$ )		$V_{(BR)DSS}$	500			V
Drain-Source Leakage Current ( $V_{DS} = 500V, V_{GS} = 0V$ )		$I_{DSS}$			1	$\mu A$
Gate-Source Leakage Current-Forward ( $V_{gsf} = 20V, V_{DS} = 0V$ )		$I_{GSSF}$			100	nA
Gate-Source Leakage Current-Reverse ( $V_{gsr} = -20V, V_{DS} = 0V$ )		$I_{GSSR}$			100	nA
Gate Threshold Voltage ( $V_{DS} = V_{GS}, I_D = 250 \mu A$ )		$V_{GS(th)}$	2	3	4	V
Static Drain-Source On-Resistance ( $V_{GS} = 10V, I_D = 6.2A$ ) *		$R_{DS(on)}$			0.19	$\Omega$
Gate input resistance		$R_G$		2.748		$\Omega$
Input Capacitance	$(V_{DS} = 100V, V_{GS} = 0V, f = 1.0 \text{ MHz})$	$C_{iss}$		1036		pF
Output Capacitance		$C_{oss}$		50		pF
Reverse Transfer Capacitance		$C_{rss}$		38		pF
Turn-On Delay Time	$(V_{DD} = 250V, I_D = 6.2A, V_{GS} = 10V, R_G = 9.1\Omega) *$	$t_{d(on)}$		14.6		ns
Rise Time		$t_r$		43.2		ns
Turn-Off Delay Time		$t_{d(off)}$		64.6		ns
Fall Time		$t_f$		24		ns
Total Gate Charge	$(V_{DS} = 400V, I_D = 6.2A, V_{GS} = 10V) *$	$Q_g$		25.75		nC
Gate-Source Charge		$Q_{gs}$		6.76		nC
Gate-Drain Charge		$Q_{gd}$		9.38		nC
<b>SOURCE-DRAIN DIODE CHARACTERISTICS</b>						
Forward On-Voltage(1)	$(I_S = 6.2 A, di_S/dt = 100A/\mu s)$	$V_{SD}$			1.5	V
Forward Turn-On Time		$t_{on}$		**		ns
Reverse Recovery Time		$t_{rr}$		261.533		ns

\* Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$

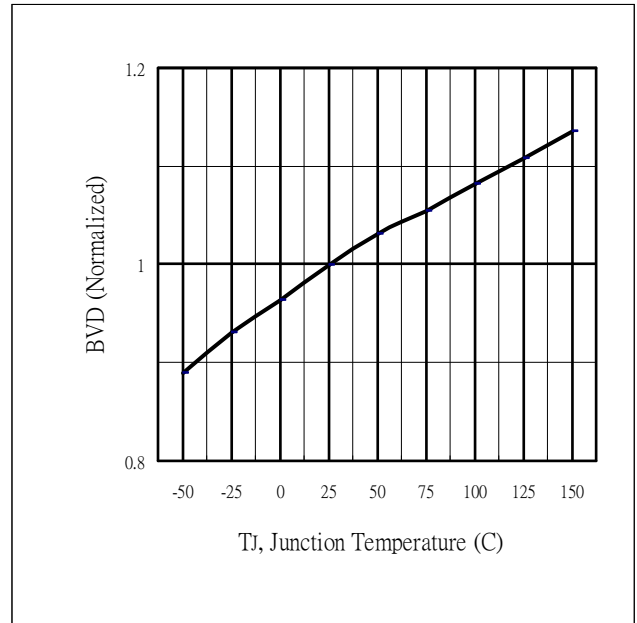
\*\* Negligible, Dominated by circuit inductance



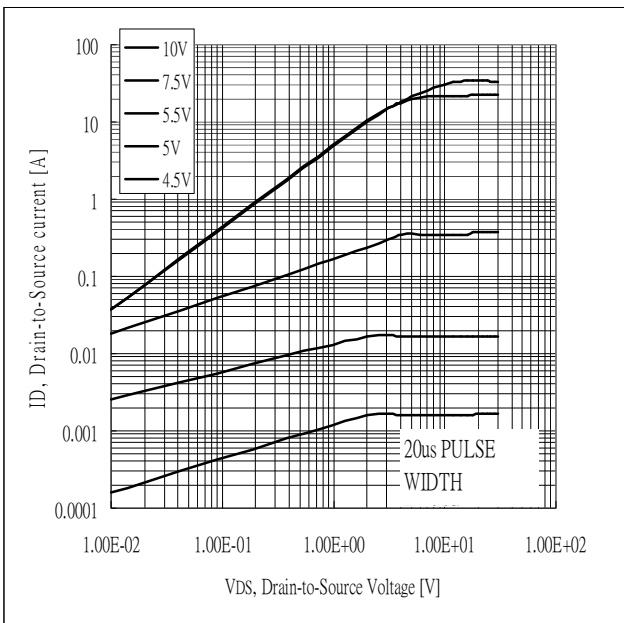
### TYPICAL ELECTRICAL CHARACTERISTICS



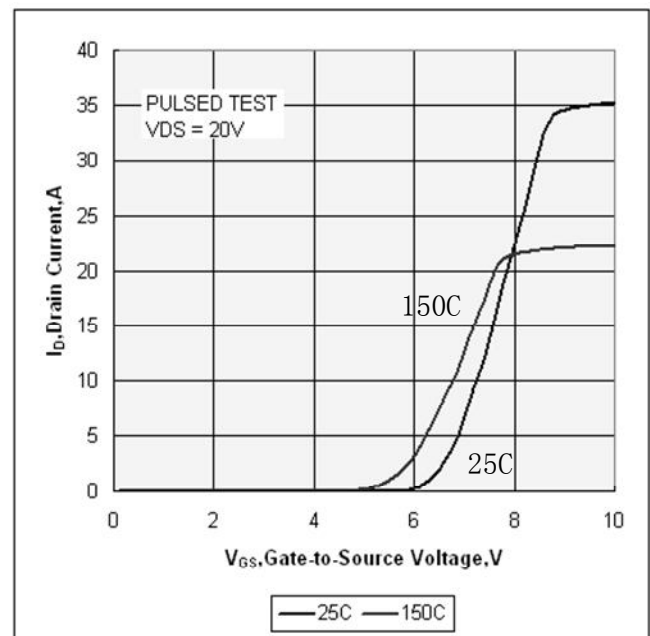
**Fig 1. On-Resistance Variation with vs. Temperature**



**Fig.2 Breakdown Voltage Variation vs. Temperature**



**Fig 3. Typical Output Characteristics**

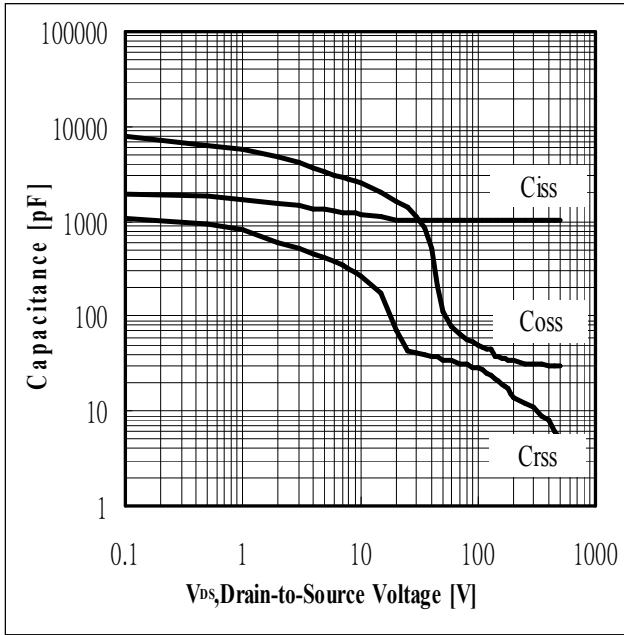


**Fig 4. Typical Transfer Characteristics**

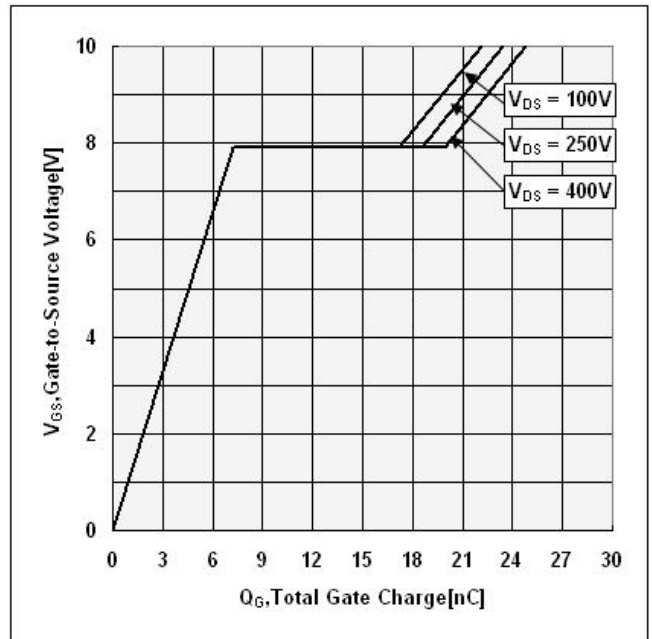


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**Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage**



**Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage**